

10/025,217
Attorney Docket No.: 42P12564

Remarks:

Reconsideration of the above referenced application in view of the enclosed amendment and remarks is requested. Claims 2 and 6 has been cancelled. Claims 1, 7-9, 11, 14, and 16-22 have been amended. Existing Claims 1, 3-5 and 7-22 remain in the application.

ARGUMENT

Claims 2, 7, 8, 11, 14, 15, 16, 18, 21 and 22 are rejected under 35 U.S.C. § 112, second paragraph. This rejection is believed moot based on the above amendments.

Claims 1, 3, 4, 6, 9, 11, 13, 16, 18 and 20 are rejected under 35 U.S.C. § 103(a) as being unpatentable over USPN 6,397,242 to Devine (hereinafter "Devine") in view of "*The Technology Behind Crusoe Processors*" by Alexander Klaiber (hereinafter "Klaiber").

Claims 2, 10 and 17 are rejected under 35 U.S.C. § 103(a) as being unpatentable over Devine and Klaiber in view of USPN 6,496,847 to Bugnion (hereinafter "Bugnion").

Claims 5, 12 and 19 are rejected under 35 U.S.C. § 103(a) as being unpatentable over Devine and Klaiber in view of "*Running multiple operating systems concurrently on an IA32 PC using virtualization techniques*" by Kevin Lawton (hereinafter "Lawton").

Claims 7, 8, 14, 15, 21 and 22 are rejected under 35 U.S.C. § 103(a) as being unpatentable over Devine and Klaiber in view of "*Operating systems internals and design principles*" by William Stallings (hereinafter "Stallings").

These rejections are respectfully traversed and Claims 1, 3-5 and 7-22 are believed allowable as amended based on the following discussion.

Independent Claims 1, 9 and 16 are amended to require that *an operating system executing on the host machine also supports a full platform simulator that includes device models*. The Examiner contends in paragraph 6 of the Office Action that Bugnion supports a full platform simulator. This assertion is in error. Bugnion shows device emulators and I/O emulation modules for use with a virtual machine monitor (VMM). Bugnion shows that a VMM supports emulation of some I/O and devices for capturing a virtual machine's I/O. This is in contrast to Applicants' claimed invention. Figure 1 shows that the software simulator (SoftSDV)

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is part of the Host Environment 101. The VMM is shown as part of the Direct Execution Environment 102. Applicants' claims require that the operating system on the host machine (not the VMM) provide the full platform simulator. This enables instruction set architectures (ISAs) to be simulated using VMs. The full platform simulator simulates the full PC hardware platform, as described in the specification. This simulation of the full platform is not taught or suggested by any of the cited references. Further, application of Bugnion to Devine and Klaiber will not result in a virtualized platform that enables ISA simulation as does Applicants' claimed invention.

Thus, all claims remaining in the application are now allowable.

CONCLUSION

In view of the foregoing, Claims 1, 3-5 and 7-22 are all in condition for allowance. If the Examiner has any questions, the Examiner is invited to contact the undersigned at (703) 633-6845. Early issuance of Notice of Allowance is respectfully requested. Please charge any shortage of fees in connection with the filing of this paper, including extension of time fees, to Deposit Account 02-2666 and please credit any excess fees to such account.

Respectfully submitted,

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